



Séminaire Informatique Haute Performance au Campus Ter@tec  
Département Sciences de la Simulation et de l'Information

**Jeudi 22 Mars 2018**

**11h00 – durée : 20min**

**Bâtiment Ter@tec  
Salle Gauguin – 2<sup>ème</sup> étage**

***READEX – Runtime Exploitation of Application Dynamism for Energy-efficient eXascale computing.***

**Uldis Locans, HPC Software engineer à Intel France**

The importance of energy efficiency is constantly increasing in High Performance Computing (HPC). While systems can be adapted to individual applications in order to reduce energy consumption, manual tuning of platform parameters is a tedious and often neglected task. The READEX project automates this by developing a tools-aided methodology for dynamic auto-tuning that combines technologies from two ends of the computing spectrum: system scenario methodology from the embedded world and auto-tuning from the field of HPC.

**Biographie:**

Uldis Locans is a HPC software engineer at Intel France. He earned his PhD from University of Latvia in collaboration with Paul Scherrer Institute in Switzerland, working on use of new hardware architectures, for optimization of particle accelerator simulations and experimental data analysis tools. His current work involves research on tools and methods for energy efficiency optimization in HPC applications.